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   IBM Journal of Research and Development issue 46-45, IBM
   eServer z900 - IBM eServer z900 high-frequency microprocessor
   technology, circuits, and design **methodology**
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2. **curran.txt**
   using a semicustom design **methodology** that employed circuit
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4. **allen.txt**
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   through a circuit, and **EinsTLT** fills that role in IBM's ...
   piecewise-linear techniques. **EinsTLT** runs on a flat netlist
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6. **EDA in IBM: past, present, and future - Computer-Aided Design of Integrated Circuits...**
   have created a highly efficient design **methodology**, sup-ported
   by a tightly integrated ... laid the foundation for a verification
   methodology for the next two decades. The 3081 was

URL: http://researchweb.watson.ibm.com/compsci/spotlight/...

7.   **IBM Think Research | Chip Checker**
     timing, or delay. A static transistor-level timer code-named
     EinsTLT traces the paths to determine whether the circuit meets
     its
     URL: http://www.research.ibm.com/thinkresearch/pages/2002...

8.   **20020625_einstuner.html**
     timing, or delay. A static transistor-level timer code-named
     EinsTLT traces the paths to determine whether the circuit meets
     its
     URL: http://domino.watson.ibm.com/comm/wwwr_thinkresearch...

9.   **IBM Course description: WebSphere Performance Tools and
     Methodology Workshop - IT...**
     Course description WebSphere Performance Tools and
     Methodology Workshop ... description: WebSphere Performance
     Tools and **Methodology** Workshop Course code Skill level
     Intermediate
     URL: http://www-304.ibm.com/jct03001c/services/learning/i...

10. **IBM Redbooks | Troubleshooting methodology with DB2
     Warehouse ManagerIBM Redbooks**
     This document describes the 3-phase generic troubleshooting
     methodology and the logical flow to go through to solve any
     problem that may be encountered when using DB2 Warehouse
     Manager V7.2.
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